INFORMATION DISCLOSURE CITATION IN AN APPLICATION

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APPLICANT: **Yasuhiko TSUKIKAWA**	
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U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Cond	Tetsuya IIZUKA, BAIFUKAN, *"Design of CMOS Ultra LSI"*, April 25, 1989, p. 128.

EXAMINER: _____ DATE CONSIDERED: 1/11/2002

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.